

H-727-05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

K. TANAKA et al.

Serial No. 10/647,280

Group Art Unit: 2819

Filed: August 26, 2003

Examiner: V. Tan

For: LEVEL CONVERSION CIRCUIT AND
SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE EMPLOYING THE LEVEL
CONVERSION CIRCUIT

REPLY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 30, 2004

Sir:

In response to the Office Action dated April 28, 2004,
the Applicants request reconsideration as follows. A Petition
and fee for a one-month Extension of Time also accompanies
this response.

BEST AVAILABLE COPY